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A Study on CMOS Power Amplifier for Wireless communication

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Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | As modern wireless communication technology evolves, RF power amplifiers (PAs) becomes a hot issue, because they are the key component of wireless mobile communication systems and the communication quality strongly depends on performance of the RF PAs. Modern wireless communication systems such as CDMA2000, WCDMA, OFDM, and so on, are intended to maximize data rate in a fast moving environment. The modulated signals of these systems require both good linearity and efficiency while the signals have a high peak to average power ratio. So the linearity and efficiency are two the most important characteristics of PAs for wireless applications. Recently, increasing numbers of RF components are being integrated using the CMOS process to meet the requirements of low cost and small size in wireless consumer markets. However, the CMOS power amplifier is difficult to design because of the low breakdown voltage, conductive Si substrate and lack of ground via. All make design of a full-integrated PA extremely challenging. This book focuses on efficiency enhancement and linearization techniques for CMOS power amplifiers design. | Format: Paperback | Language/Sprache: english | 92 pp.



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